



AiP74AUP1G07

Low Power Single Buffer (Open-drain)

Product Specification

Specification Revision History:

Version	Date	Description
2019-09-A1	2019-09	New
2023-04-B1	2023-04	Update the template



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1、General Description

The AiP74AUP1G07 provides the single non-inverting buffer with open-drain output. The output of the device is an open drain and can be connected to other open-drain outputs to implement active-LOW wired-OR or active-HIGH wired-AND functions.

This device ensures a very low static and dynamic power consumption across the entire V_{CC} range from 0.8V to 3.6V.

This device is fully specified for partial power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

Features:

- Wide supply voltage range from 0.8V to 3.6V
- Low static power consumption; $I_{CC}=1\mu A$ (maximum)
- Inputs accept voltages up to 3.6V
- I_{OFF} circuitry provides partial Power-down mode operation
- Specified from $-40^{\circ}C$ to $+125^{\circ}C$
- Packaging information: SOT23-5/SOT353

Ordering Information:

Reel packing specifications:

Part number	Packaging form	Marking code	Reel quantity	Boxed reel quantity	Notes
AiP74AUP1G07GB235.TR	SOT23-5	DHXX	3000PCS/reel	30000PCS/box	Dimensions of plastic enclosure: 2.9mm×1.6mm Pin spacing: 0.95mm
AiP74AUP1G07GC353.TR	SOT353	DHXX	3000PCS/reel	30000PCS/box	Dimensions of plastic enclosure: 2.1mm×1.3mm Pin spacing: 0.65mm

Note 1: "XX" refers to variable content, meaning year and package batch serial number.

Note 2: If the physical information is inconsistent with the ordering information, please refer to the actual product.



2、Block Diagram And Pin Description

2.1、Block Diagram

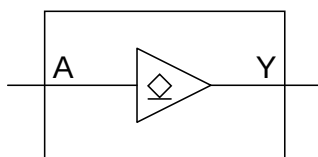


Figure 1. Logic symbol

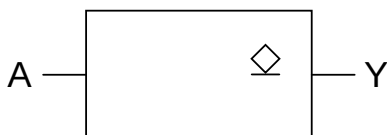


Figure 2. IEC logic symbol

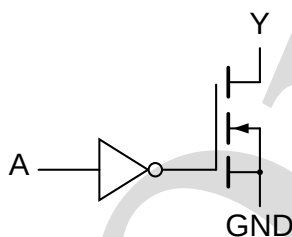


Figure 3. Logic diagram

2.2、Pin Configurations

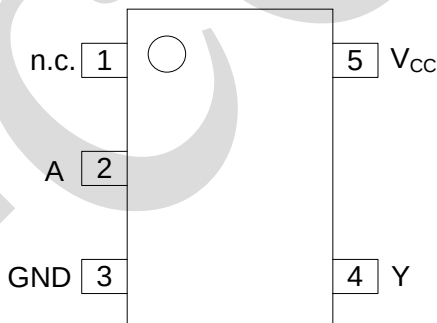


Figure 4. Pin Configuration

2.3、Pin Description

Pin No.	Pin Name	Description
1	n.c.	not connected
2	A	data input
3	GND	ground (0V)
4	Y	data output
5	V _{CC}	supply voltage



2.4、Function Table

Input	Output
A	Y
L	L
H	Z

Note: H=HIGH voltage level; L=LOW voltage level; Z=high-impedance OFF state.

3、Electrical Parameter

3.1、Absolute Maximum Ratings

($T_{amb}=25^{\circ}\text{C}$, voltages are referenced to GND (ground=0V), unless otherwise specified)

Characteristic	Symbol	Conditions	Min.	Max.	Unit
supply voltage	V_{CC}	-	-0.5	+4.6	V
input clamping current	I_{IK}	$V_I < 0V$	-50	-	mA
input voltage	V_I	-	-0.5	+4.6	V
output clamping current	I_{OK}	$V_O < 0V$	-50	-	mA
output voltage	V_O	active mode and power-down mode	-0.5	+4.6	V
output current	I_O	$V_O = 0V$ to V_{CC}	-	20	mA
supply current	I_{CC}	-	-	50	mA
ground current	I_{GND}	-	-50	-	mA
storage temperature	T_{stg}	-	-65	+150	$^{\circ}\text{C}$
total power dissipation	P_{tot}	-	-	250	mW
soldering temperature	T_L	10s	260		$^{\circ}\text{C}$

3.2、Recommended Operating Conditions

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
supply voltage	V_{CC}	-	0.8	-	3.6	V
input voltage	V_I	-	0	-	3.6	V
output voltage	V_O	active mode and power-down mode	0	-	3.6	V
ambient temperature	T_{amb}	-	-40	-	+125	$^{\circ}\text{C}$
input transition rise and fall rate	$\Delta t/\Delta V$	$V_{CC}=0.8V$ to $3.6V$	0	-	200	ns/V



3.3、Electrical Characteristics

3.3.1、DC Characteristics 1

($T_{amb}=25^{\circ}\text{C}$, voltages are referenced to GND (ground=0V), unless otherwise specified)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
HIGH-level input voltage	V _{IH}	V _{CC} =0.8V		0.70×V _{CC}	-	-	V
		V _{CC} =0.9V to 1.95V		0.65×V _{CC}	-	-	V
		V _{CC} =2.3V to 2.7V		1.6	-	-	V
		V _{CC} =3.0V to 3.6V		2.0	-	-	V
LOW-level input voltage	V _{IL}	V _{CC} =0.8V		-	-	0.30×V _{CC}	V
		V _{CC} =0.9V to 1.95V		-	-	0.35×V _{CC}	V
		V _{CC} =2.3V to 2.7V		-	-	0.7	V
		V _{CC} =3.0V to 3.6V		-	-	0.9	V
LOW-level output voltage	V _{OL}	V _I =V _{IH} or V _{IL}	I _O =20uA; V _{CC} =0.8V to 3.6V	-	-	0.1	V
			I _O =1.1mA; V _{CC} =1.1V	-	-	0.3×V _{CC}	V
			I _O =1.7mA; V _{CC} =1.4V	-	-	0.31	V
			I _O =1.9mA; V _{CC} =1.65V	-	-	0.31	V
			I _O =2.3mA; V _{CC} =2.3V	-	-	0.31	V
			I _O =3.1mA; V _{CC} =2.3V	-	-	0.44	V
			I _O =2.7mA; V _{CC} =3.0V	-	-	0.31	V
			I _O =4.0mA; V _{CC} =3.0V	-	-	0.44	V
input leakage current	I _I	V _I =GND to 3.6V; V _{CC} =0V to 3.6V		-	-	±1.0	uA
OFF-state output current	I _{OZ}	V _I =V _{IH} ; V _O =0V to 3.6V; V _{CC} =0V to 3.6V		-	-	±1.0	uA
power-off leakage current	I _{OFF}	V _I or V _O =0V to 3.6V; V _{CC} =0V		-	-	±1.0	uA
additional power-off leakage current	ΔI _{OFF}	V _I or V _O =0V to 3.6V; V _{CC} =0V to 0.2V		-	-	±1.0	uA
supply current	I _{CC}	V _I =GND or V _{CC} ; I _O =0A; V _{CC} =0.8V to 3.6V		-	-	1.0	uA
additional supply current	ΔI _{CC}	V _I =V _{CC} -0.6V; I _O =0A; V _{CC} =3.3V		-	-	40	uA
input capacitance	C _I	V _{CC} =0V to 3.6V; V _I =GND or V _{CC}		-	0.8	-	pF
output capacitance	C _O	output enabled; V _O =GND; V _{CC} =0V		-	1.7	-	pF
		output disabled; V _O =GND; V _{CC} =0V		-	1.1	-	pF



3.3.2、DC Characteristics 2

($T_{amb} = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, voltages are referenced to GND (ground=0V), unless otherwise specified)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
HIGH-level input voltage	V_{IH}	$V_{CC}=0.8\text{V}$		$0.70 \times V_{CC}$	-	-	V
		$V_{CC}=0.9\text{V}$ to 1.95V		$0.65 \times V_{CC}$	-	-	V
		$V_{CC}=2.3\text{V}$ to 2.7V		1.6	-	-	V
		$V_{CC}=3.0\text{V}$ to 3.6V		2.0	-	-	V
LOW-level input voltage	V_{IL}	$V_{CC}=0.8\text{V}$		-	-	$0.30 \times V_{CC}$	V
		$V_{CC}=0.9\text{V}$ to 1.95V		-	-	$0.35 \times V_{CC}$	V
		$V_{CC}=2.3\text{V}$ to 2.7V		-	-	0.7	V
		$V_{CC}=3.0\text{V}$ to 3.6V		-	-	0.9	V
LOW-level output voltage	V_{OL}	$V_I = V_{IH}$ or V_{IL}	$I_O=20\mu\text{A}; V_{CC}=0.8\text{V}$ to 3.6V	-	-	0.1	V
			$I_O=1.1\text{mA}; V_{CC}=1.1\text{V}$	-	-	$0.3 \times V_{CC}$	V
			$I_O=1.7\text{mA}; V_{CC}=1.4\text{V}$	-	-	0.37	V
			$I_O=1.9\text{mA}; V_{CC}=1.65\text{V}$	-	-	0.35	V
			$I_O=2.3\text{mA}; V_{CC}=2.3\text{V}$	-	-	0.33	V
			$I_O=3.1\text{mA}; V_{CC}=2.3\text{V}$	-	-	0.45	V
			$I_O=2.7\text{mA}; V_{CC}=3.0\text{V}$	-	-	0.33	V
			$I_O=4.0\text{mA}; V_{CC}=3.0\text{V}$	-	-	0.45	V
input leakage current	I_I	$V_I = \text{GND}$ to $3.6\text{V}; V_{CC}=0\text{V}$ to 3.6V		-	-	± 1.0	μA
OFF-state output current	I_{OZ}	$V_I = V_{IH}; V_O=0\text{V}$ to $3.6\text{V}; V_{CC}=0\text{V}$ to 3.6V		-	-	± 1.0	μA
power-off leakage current	I_{OFF}	V_I or $V_O=0\text{V}$ to $3.6\text{V}; V_{CC}=0\text{V}$		-	-	± 1.0	μA
additional power-off leakage current	ΔI_{OFF}	V_I or $V_O=0\text{V}$ to $3.6\text{V}; V_{CC}=0\text{V}$ to 0.2V		-	-	± 1.0	μA
supply current	I_{CC}	$V_I = \text{GND}$ or $V_{CC}; I_O=0\text{A}; V_{CC}=0.8\text{V}$ to 3.6V		-	-	1.0	μA
additional supply current	ΔI_{CC}	$V_I = V_{CC}-0.6\text{V}; I_O=0\text{A}; V_{CC}=3.3\text{V}$		-	-	50	μA



3.3.3、DC Characteristics 3

($T_{amb} = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$, voltages are referenced to GND (ground=0V), unless otherwise specified)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
HIGH-level input voltage	V _{IH}	V _{CC} =0.8V		0.75×V _{CC}	-	-	V
		V _{CC} =0.9V to 1.95V		0.70×V _{CC}	-	-	V
		V _{CC} =2.3V to 2.7V		1.6	-	-	V
		V _{CC} =3.0V to 3.6V		2.0	-	-	V
LOW-level input voltage	V _{IL}	V _{CC} =0.8V		-	-	0.25×V _{CC}	V
		V _{CC} =0.9V to 1.95V		-	-	0.30×V _{CC}	V
		V _{CC} =2.3V to 2.7V		-	-	0.7	V
		V _{CC} =3.0V to 3.6V		-	-	0.9	V
LOW-level output voltage	V _{OL}	V _I =V _{IH} or V _{IL}	I _O =20uA; V _{CC} =0.8V to 3.6V	-	-	0.11	V
			I _O =1.1mA; V _{CC} =1.1V	-	-	0.33×V _{CC}	V
			I _O =1.7mA; V _{CC} =1.4V	-	-	0.41	V
			I _O =1.9mA; V _{CC} =1.65V	-	-	0.39	V
			I _O =2.3mA; V _{CC} =2.3V	-	-	0.36	V
			I _O =3.1mA; V _{CC} =2.3V	-	-	0.50	V
			I _O =2.7mA; V _{CC} =3.0V	-	-	0.36	V
			I _O =4.0mA; V _{CC} =3.0V	-	-	0.50	V
input leakage current	I _I	V _I =GND to 3.6V; V _{CC} =0V to 3.6V		-	-	±1.0	uA
OFF-state output current	I _{OZ}	V _I =V _{IH} ; V _O =0V to 3.6V; V _{CC} =0V to 3.6V		-	-	±1.0	uA
power-off leakage current	I _{OFF}	V _I or V _O =0V to 3.6V; V _{CC} =0V		-	-	±1.0	uA
additional power-off leakage current	ΔI _{OFF}	V _I or V _O =0V to 3.6V; V _{CC} =0V to 0.2V		-	-	±1.0	uA
supply current	I _{CC}	V _I =GND or V _{CC} ; I _O =0A; V _{CC} =0.8V to 3.6V		-	-	1.4	uA
additional supply current	ΔI _{CC}	V _I =V _{CC} -0.6V; I _O =0A; V _{CC} =3.3V		-	-	75	uA



3.3.4、AC Characteristics 1

($T_{amb}=25^{\circ}\text{C}$, voltages are referenced to GND (ground=0V), unless otherwise specified)

Parameter	Symbol	Conditions		Min.	Typ. ^[1]	Max.	Unit
propagation delay	t _{pd}	A to Y; see Figure 6 ^[2]	C _L =5pF				
			V _{CC} =0.8V	-	11.6	-	ns
			V _{CC} =1.1V to 1.3V	2.1	4.1	7.5	ns
			V _{CC} =1.4V to 1.6V	1.6	3.0	5.1	ns
			V _{CC} =1.65V to 1.95V	1.6	2.7	4.0	ns
			V _{CC} =2.3V to 2.7V	1.1	2.1	3.2	ns
			V _{CC} =3.0V to 3.6V	1.4	2.2	2.8	ns
			C _L =10pF				
			V _{CC} =0.8V	-	14.7	-	ns
			V _{CC} =1.1V to 1.3V	3.0	5.1	9.0	ns
			V _{CC} =1.4V to 1.6V	2.3	3.8	6.1	ns
			V _{CC} =1.65V to 1.95V	2.4	3.6	4.8	ns
			V _{CC} =2.3V to 2.7V	1.7	2.8	3.8	ns
			V _{CC} =3.0V to 3.6V	2.2	3.1	4.2	ns
			C _L =15pF				
			V _{CC} =0.8V	-	17.7	-	ns
			V _{CC} =1.1V to 1.3V	3.5	6.1	10.4	ns
			V _{CC} =1.4V to 1.6V	3.0	4.5	6.8	ns
			V _{CC} =1.65V to 1.95V	2.8	4.4	6.7	ns
			V _{CC} =2.3V to 2.7V	2.4	3.4	4.5	ns
			V _{CC} =3.0V to 3.6V	2.2	4.0	5.7	ns
			C _L =30pF				
			V _{CC} =0.8V	-	24.6	-	ns
			V _{CC} =1.1V to 1.3V	4.8	9.0	15.6	ns
			V _{CC} =1.4V to 1.6V	4.1	6.7	9.4	ns
			V _{CC} =1.65V to 1.95V	3.8	6.8	9.7	ns
			V _{CC} =2.3V to 2.7V	3.7	5.2	6.7	ns
			V _{CC} =3.0V to 3.6V	3.6	6.4	9.7	ns
power dissipation capacitance	C _{PD}	f=1MHz; V _I =GND to V _{CC} ^[3]	V _{CC} =0.8V	-	0.5	-	pF
			V _{CC} =1.1V to 1.3V	-	0.6	-	pF
			V _{CC} =1.4V to 1.6V	-	0.6	-	pF
			V _{CC} =1.65V to 1.95V	-	0.7	-	pF
			V _{CC} =2.3V to 2.7V	-	0.9	-	pF
			V _{CC} =3.0V to 3.6V	-	1.2	-	pF

Note:

[1] All typical values are measured at nominal V_{CC} .

[2] t_{pd} is the same as t_{PLZ} and t_{PZL} .

[3] C_{PD} is used to determine the dynamic power dissipation (P_D in uW).

$P_D=C_{PD}\times V_{CC}^2\times f_i\times N$ where:

f_i =input frequency in MHz;

V_{CC} =supply voltage in V;

N =number of inputs switching.



3.3.5、AC Characteristics 2

($T_{amb} = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$, voltages are referenced to GND (ground=0V), unless otherwise specified)

Parameter	Symbol	Conditions		Min.	Max. (85°C)	Max. (125°C)	Unit
propagation delay	t _{pd}	A to Y; see Figure 6 ^[2]	C _L =5pF				
			V _{CC} =1.1V to 1.3V	1.7	9.1	10.0	ns
			V _{CC} =1.4V to 1.6V	1.3	6.1	6.7	ns
			V _{CC} =1.65V to 1.95V	1.2	5.0	5.5	ns
			V _{CC} =2.3V to 2.7V	0.9	4.0	4.4	ns
			V _{CC} =3.0V to 3.6V	1.1	3.3	3.6	ns
			C _L =10pF				
			V _{CC} =1.1V to 1.3V	2.4	11.2	12.3	ns
			V _{CC} =1.4V to 1.6V	2.0	7.4	8.1	ns
			V _{CC} =1.65V to 1.95V	1.8	6.1	6.7	ns
			V _{CC} =2.3V to 2.7V	1.3	4.8	5.3	ns
			V _{CC} =3.0V to 3.6V	1.6	4.5	5.0	ns
			C _L =15Pf				
			V _{CC} =1.1V to 1.3V	3.2	13.1	14.5	ns
			V _{CC} =1.4V to 1.6V	2.6	8.6	9.4	ns
			V _{CC} =1.65V to 1.95V	2.2	7.8	8.6	ns
			V _{CC} =2.3V to 2.7V	1.9	5.3	5.8	ns
			V _{CC} =3.0V to 3.6V	1.9	6.1	6.7	ns
			C _L =30pF				
			V _{CC} =1.1V to 1.3V	4.3	18.8	20.7	ns
			V _{CC} =1.4V to 1.6V	3.7	11.8	13.0	ns
			V _{CC} =1.65V to 1.95V	3.2	11.0	12.1	ns
			V _{CC} =2.3V to 2.7V	3.0	7.1	7.8	ns
			V _{CC} =3.0V to 3.6V	2.8	10.4	11.4	ns

Note:

[1] All typical values are measured at nominal V_{CC} .

[2] t_{pd} is the same as t_{PLZ} and t_{PZL} .



4、Testing Circuit

4.1、AC Testing Circuit

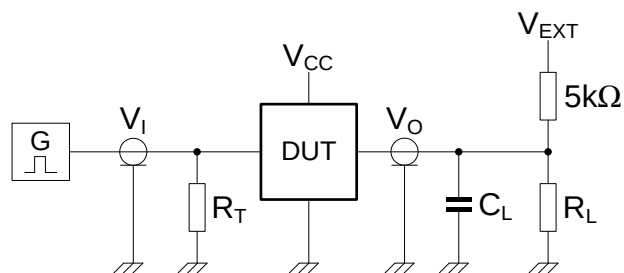


Figure 5. Test circuit for measuring switching times

Definitions for test circuit:

R_L =Load resistance.

C_L =Load capacitance including jig and probe capacitance.

R_T =Termination resistance should be equal to the output impedance Z_o of the pulse generator.

V_{EXT} =External voltage for measuring switching times.

4.2、AC Testing Waveforms

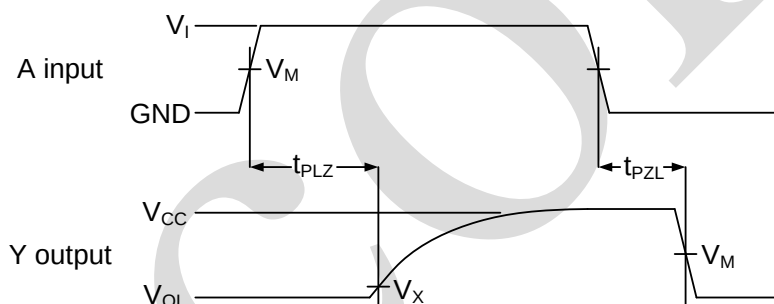


Figure 6. The data input (A) to output (Y) propagation delays

4.3、Measurement Points

Supply voltage	Input	Output	
V_{CC}	V_M	V_M	V_X
0.8V to 1.6V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	$V_{OL} + 0.1V$
1.65V to 2.7V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	$V_{OL} + 0.15V$
3.0V to 3.6V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	$V_{OL} + 0.3V$

4.4、Test Data

Supply voltage	Load		V_{EXT}		
V_{CC}	C_L	$R_L^{[1]}$	t_{PLH}, t_{PHL}	t_{PZH}, t_{PHZ}	t_{PZL}, t_{PLZ}
0.8V to 3.6V	5pF, 10pF, 15pF and 30pF	5kΩ or 1MΩ	open	GND	$2 \times V_{CC}$

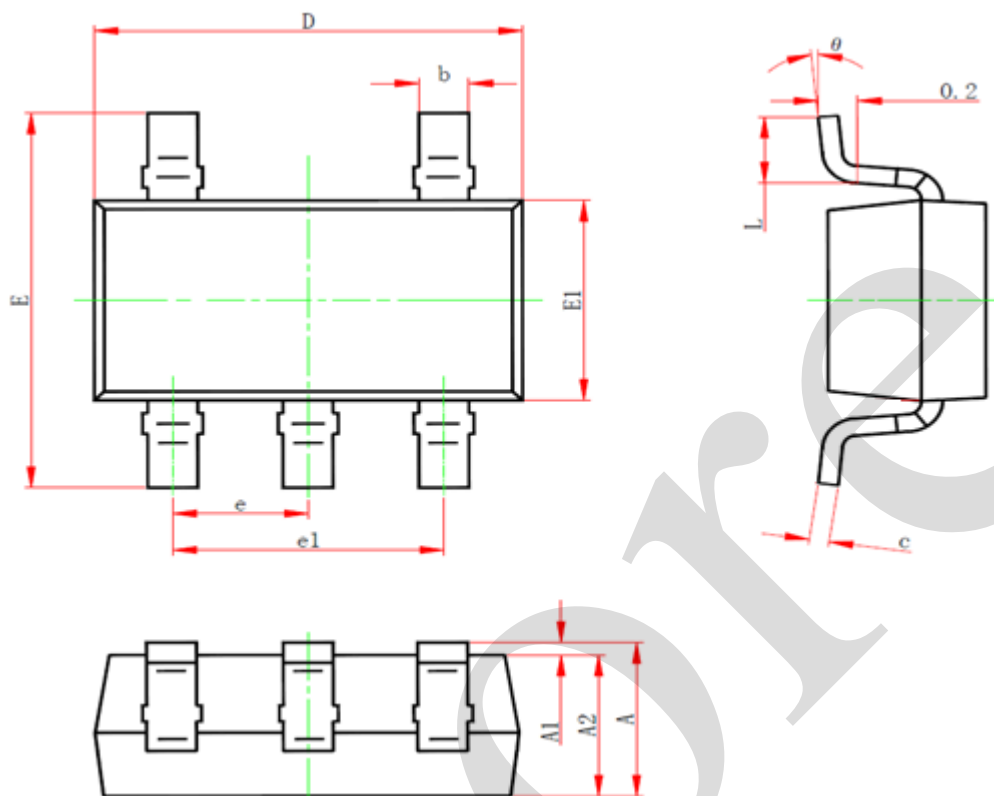
Note:

[1] For measuring enable and disable times $R_L=5k\Omega$, for measuring propagation delays, setup and hold times and pulse width $R_L=1M\Omega$.



5、Package Information

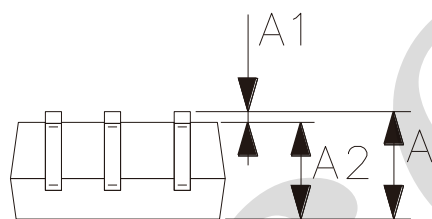
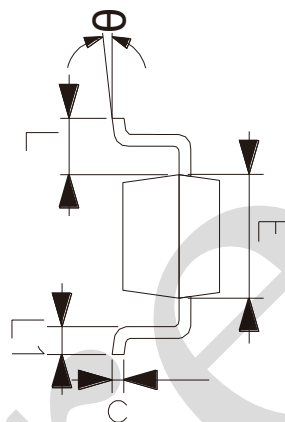
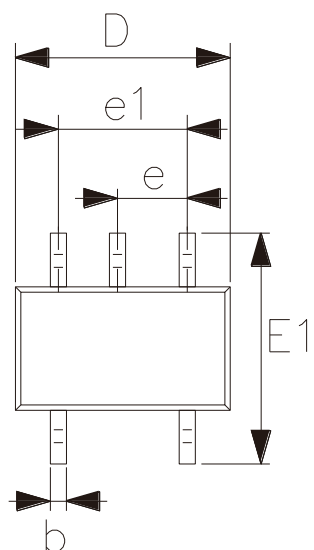
5.1、SOT23-5



Symbol	Dimensions (mm)	
	Min.	Max.
A	-	1.26
A1	0.00	0.12
A2	1.00	1.20
b	0.30	0.50
c	0.10	0.20
D	2.82	3.02
E	2.60	3.00
E1	1.50	1.70
e	0.95	
e1	1.80	2.00
L	0.30	0.60
θ	0°	8°



5.2、SOT353



Symbol	Dimensions (mm)	
	Min.	Max.
A	0.90	1.10
A1	0.00	0.10
A2	0.90	1.00
b	0.15	0.35
c	0.11	0.175
D	2.00	2.20
E	1.15	1.35
E1	2.15	2.45
e	0.65	
e1	1.20	1.40
L	0.525	
L1	0.26	0.46
θ	0°	8°



6、Statements And Notes

6.1、The name and content of Hazardous substances or Elements in the product

Part name	Hazardous substances or Elements									
	Lead and lead compounds	Mercury and mercury compounds	Cadmium and cadmium compounds	Hexavalent chromium compounds	Polybrominated biphenyls	Polybrominated biphenyl ethers	Dibutyl phthalate	Butylbenzyl phthalate	Di-2-ethylhexyl phthalate	Diisobutyl phthalate
Lead frame	○	○	○	○	○	○	○	○	○	○
Plastic resin	○	○	○	○	○	○	○	○	○	○
Chip	○	○	○	○	○	○	○	○	○	○
The lead	○	○	○	○	○	○	○	○	○	○
Plastic sheet installed	○	○	○	○	○	○	○	○	○	○
explanation	○: Indicates that the content of hazardous substances or elements in the detection limit of the following the SJ/T11363-2006 standard. ×: Indicates that the content of hazardous substances or elements exceeding the SJ/T11363-2006 Standard limit requirements.									

6.2、Notes

We Recommend you to read this chapter carefully before using this product.

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